

OFFSET-COMPENSATED SELF-BIASED DIFFERENTIAL AMPLIFIER

ABSTRACT

An apparatus comprising a differential amplifier having a first input coupled
5 to receive a voltage reference signal, a second input and an output. The apparatus
also includes an inverter whose input is coupled to the differential amplifier output.
The inverter includes first and second transistors that have source/drain regions. A
third transistor is coupled between a source/drain region of the inverter and a power
supply rail to cause a switching point of the inverter to track a common mode bias
10 voltage of the differential amplifier.

"Express Mail" mailing label number: EV 370239881 US

Date of Deposit: December 30, 2003

This paper or fee is being deposited on the date indicated above with
the United States Postal Service pursuant to 37 CFR 1.10, and is
addressed to the Commissioner for Patents, Mail Stop Patent
Application, P.O. Box 1450, Alexandria, VA 22313-1450.